

10/535065

JC14 R PCT/PTO 13 MAY 2005

FORM PTO-1449/A and B (Modified)				APPLICATION NO.: 10/535065		ATTY. DOCKET NO.: S1022.81242US00			
				FILING DATE: May 13, 2005		CONFIRMATION NO.:			
				APPLICANT: Catherine Robert et al.					
				GROUP ART UNIT:		EXAMINER:			
Sheet	I	of	I						

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
KV		5,724,505		Argade et al.	03/03/1998
KV		5,848,264		Baird et al.	12/08/1998
KV		5,974,573		Martin	10/26/1999
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FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
KV		EP	1184790	A	Mann	03/06/02	

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
KV		International Search report from corresponding International Application No. PCT/FR2002/003908, filed November 14, 2002	
KV		International Search report from a related International Application No. PCT/FR2002/003909, filed November 14, 2002	
KV		International Search Report from related International Application No. PCT/FR2002/003996, filed November 21, 2002	
KV		Nexis 5001 Forum: <u>Standard for a Global Embedded Processor Debug Interface</u> , December 15, 1999, IEEE-ISTO XP002247195	

EXAMINER: /Keith Vicary/	DATE CONSIDERED: 12/04/2006
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